| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|---|---|---|---|---|---|---|
| L1 | 4 | RTL and ((HDL) same (syntax same format)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/05 12:50 |
| L2 | 2 | RTL same ((HDL) same (syntax same format)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/05 12:51 |
| S1 | 1741 | 716/5 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/05 10:43 |
| S2 | 2257 | 716/4 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/04 13:03 |
| S3 | 0 | (716/5).ccls. and (integrated adj circuit) and ((defin$ adj task) same (test adj file)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/04 13:15 |
| S4 | 0 | (716/5).ccls. and (integrated adj circuit) and ((defin$ adj task) same (text adj file)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/04 13:16 |
| S5 | 0 | (716/5).ccls. and (integrated adj circuit) and ((case same task) same (text adj file)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/04 13:17 |
| S6 | 4 | (716/5).ccls. and (integrated adj circuit) and ((case same task) same (HDL)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/04 13:23 |
| S7 | 6 | (716/4).ccls. and (integrated adj circuit) and ((case same task) same (HDL)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/04 13:29 |

| S8 | 0 | ("716"/$).ccls. and (integrated adj circuit) and ((case same task) same (HDL) same (simulat$ same RTL)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/04 13:31 |
|---|---|---|---|---|---|---|
| S9 | 0 | (integrated adj circuit) and ((case same task) same (HDL) same (simulat$ same RTL)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/04 13:31 |
| S10 | 1 | (integrated adj circuit) and ((case same task) same (HDL) same (RTL)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/04 13:47 |
| S11 | 0 | (integrated adj circuit) and (((test adj case) same task) same (HDL) same (RTL)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/04 13:49 |
| S12 | 3 | (integrated adj circuit) and ((test adj case) same task) and (HDL) and (RTL) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/04 13:54 |
| S13 | 26 | (integrated adj circuit) and (HDL same task) and (simulat$ same RTL) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/04 15:09 |
| S14 | 3 | (integrated adj circuit) and (HDL same tasks) and (simulat$ same RTL) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/04 16:01 |
| S15 | 0 | (integrated adj circuit) and (HDL same task) and (simulat$ same RTL same (adjust$ adj task)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/04 15:58 |
| S16 | 6 | (simulat$ same RTL same goal) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/04 16:32 |
| S17 | 0 | (compiled adj tasks) same (HDL) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/04 16:33 |

| S18 | 0 | (compiled adj tasks) and (HDL) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/04 16:34 |
|---|---|---|---|---|---|---|
| S19 | 244 | (compiled same HDL) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/04 18:00 |
| S20 | 3 | (precompiled same HDL) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/04 18:00 |
| S21 | 1 | (precompiled same RTL) and HDL | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/05 12:22 |
| S22 | 524 | (verification) and (RTL) and (HDL) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/05 11:12 |
| S23 | 63 | (verification) same (RTL) same (HDL) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/05 12:11 |
| S24 | 0 | (verification) same (RTL) same (HDL same task) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/05 11:15 |
| S25 | 0 | ((verification) same (RTL) same (HDL)) and precompiled | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/05 11:19 |
| S26 | 13 | ((verification) same (RTL) same (HDL)) and (storage same processor same memory) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/05 12:10 |
| S27 | 0 | ((verification) same (RTL) same (HDL)) and (macro adj tasks) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/05 12:11 |

| S28 | 0 | ((verification) and (RTL) and (HDL)) and (macro adj tasks) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/05 12:11 |
|---|---|---|---|---|---|---|
| S29 | 12 | ((verification) same (RTL) same (HDL)) and syntax | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/05 12:12 |
| S30 | 0 | (HDL adj task) and (syntax same format) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/05 12:23 |
| S31 | 141 | (HDL) and (syntax same format) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/05 12:23 |
| S32 | 26 | (HDL) same (syntax same format) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2005/01/05 12:45 |